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APPLICANTS

Toshimasa Namekawa, Tokyo, JAPAN;

Shinji Miyano, Yokohama-shi, JAPAN;
Atsushi Suzuki, Kawasaki-shi, JAPAN;** CONTINUING DATA ***** *yes* *ap*This application is a CON of 10/342,248 01/15/2003 PAT 6,744,680
which is a DIV of 10/115,977 04/05/2002 PAT 6,529,399** FOREIGN APPLICATIONS ***** *yes* *ap*

JAPAN 2001-401302 12/28/2001

JAPAN 2002-051882 02/27/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/24/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>Erik Shung</i> Examiner's Signature Initials	JAPAN	21	15	2

ADDRESS

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TITLE

SEMICONDUCTOR DEVICE REALIZED BY USING PARTIAL SOI TECHNOLOGY

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